

Etched Selects Agnisys' IDesignSpec™ to Streamline AI Chip Development

Etched selects Agnisys' IDesignSpec™ to streamline AI chip development, enhancing efficiency and enabling nextgen transformer-based hardware innovation.

BOSTON, MA, UNITED STATES, December 19, 2024 / EINPresswire.com/ -- Agnisys, Inc., the industry leader in Chip Design Automation Solutions, proudly announces that Etched, a cutting-edge player in AI-specific hardware, has chosen Agnisys and its flagship



chosen Agnisys and its flagship solution, IDesignSpec[™] and associated IP, to enhance the development of its transformative AI Application Specific Integrated Circuits (ASICs).

Etched is building the first ASIC for transformer models, which power every major generative AI

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With Agnisys' IDesignSpec, we have a powerful tool that automates our specificationdriven workflow, allowing us to work more efficiently and eliminate risks associated with manual processes" Joon Kim, Director of TPM at Etched application today. By trading flexibility for performance, they can deliver unmatched performance for Al applications.

"With Agnisys' IDesignSpec, we have a powerful tool that automates our specification-driven workflow, allowing us to work more efficiently and eliminate risks associated with manual processes," said Joon Kim, Director of TPM at Etched. "This partnership enables us to fully concentrate on accelerating the next generation of transformer-based Al applications. We evaluated the option of using free/open-source and other commercial solutions but chose Agnisys for its comprehensive functionality and

world-class 24/7 365-day support, which is paramount when rapid go-to-market is the aim.

"Etched's vision to reshape AI hardware aligns perfectly with Agnisys' strengths in automation," said Anupam Bakshi, CEO of Agnisys. "Our IDesignSpec solution is an ideal fit for their complex

design needs, providing automation that enables accuracy and productivity while allowing Etched team to remain focused on advancing transformer-specific chip architecture."

IDesignSpec is a silicon-proven solution for automatically generating and interconnecting highly configurable and customizable intellectual property (IP) blocks from specifications. It generates customizable RTL, <u>UVM RAL models</u>, C headers, and documentation from industry-standard specifications written in <u>SystemRDL</u> or <u>IP-XACT</u>, or in accepted and easy-to-use formats such as Word and Excel. The generated RTL files include the register bus IP for popular protocols, including AMBA[®], APB, AHB, AXI, Tilelink, and CXL. IDesignSpec allows Etched teams to tackle complex design challenges seamlessly and adjust quickly to specification changes.

To learn more about Agnisys solutions or to request a demo, please visit: <u>https://www.agnisys.com/contact-us</u>.

About Agnisys

Agnisys is a provider of Electronic Design Automation (EDA) software and methodology services, solves complex front-end design, verification, and validation problems in system chip development. Its certified IDesignSpec[™] Solution Suite leverages a golden executable specification to capture and centralize registers, sequences, and connectivity for Intellectual Property (IP) and System-on-a-Chip (SoC) projects. Its intuitive user interfaces and standards-based workflows reduce risk by eliminating development errors while increasing productivity and efficiency through the automatic generation of collateral for the entire project development team. Founded in 2007, Agnisys is headquartered in Boston, Massachusetts, with R&D centers in the United States and India. Learn more at <u>www.agnisys.com</u>.

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